

FIG.3A

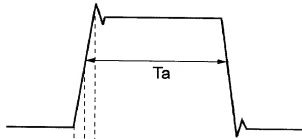


FIG.3B

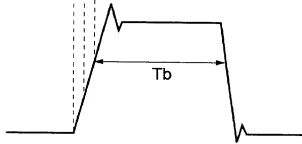


FIG.4A

WRITE PULSE

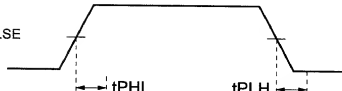


FIG.4B

IN PHASE OUTPUT

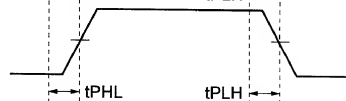
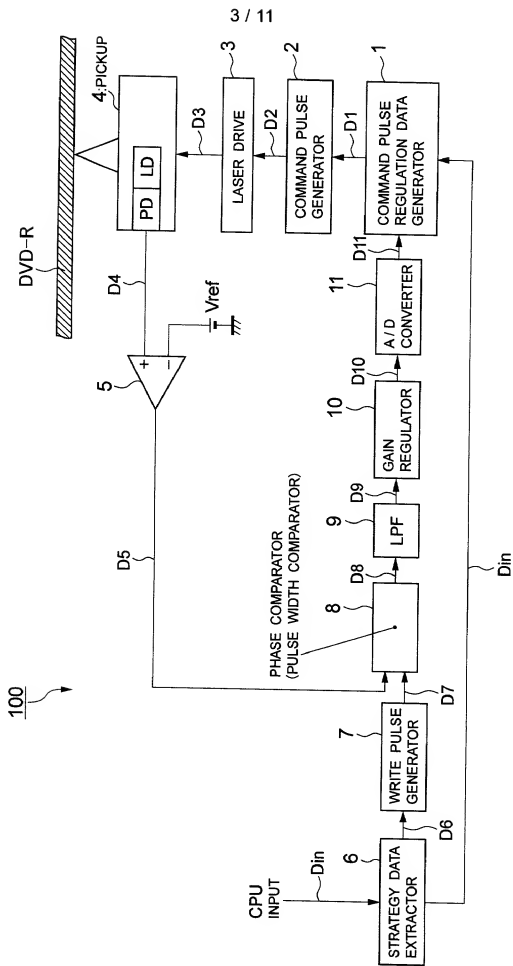


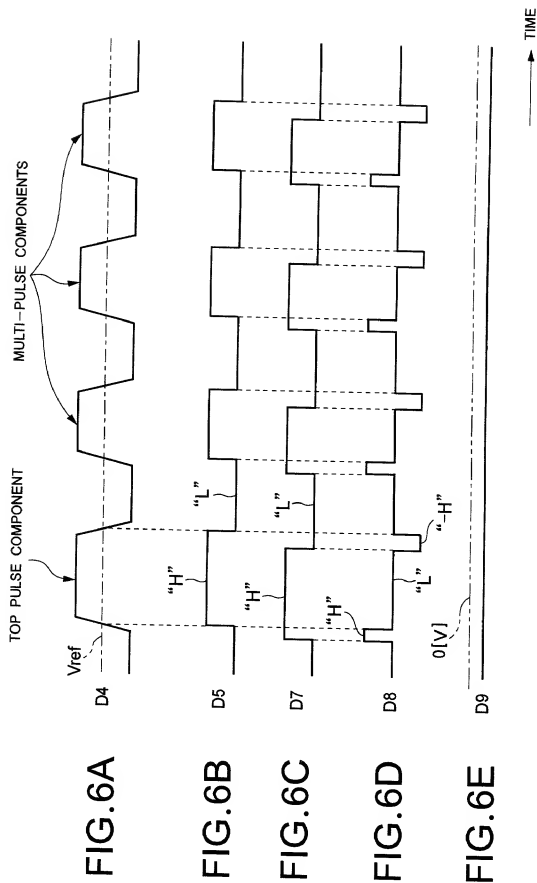
FIG.4C

INVERTED OUTPUT



FIG. 5





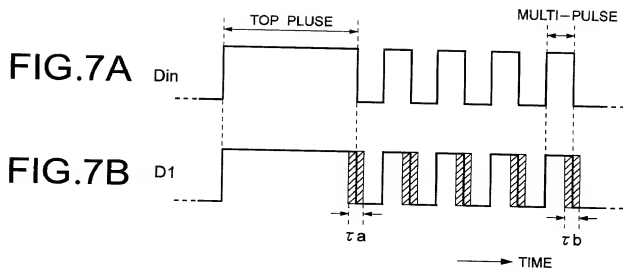
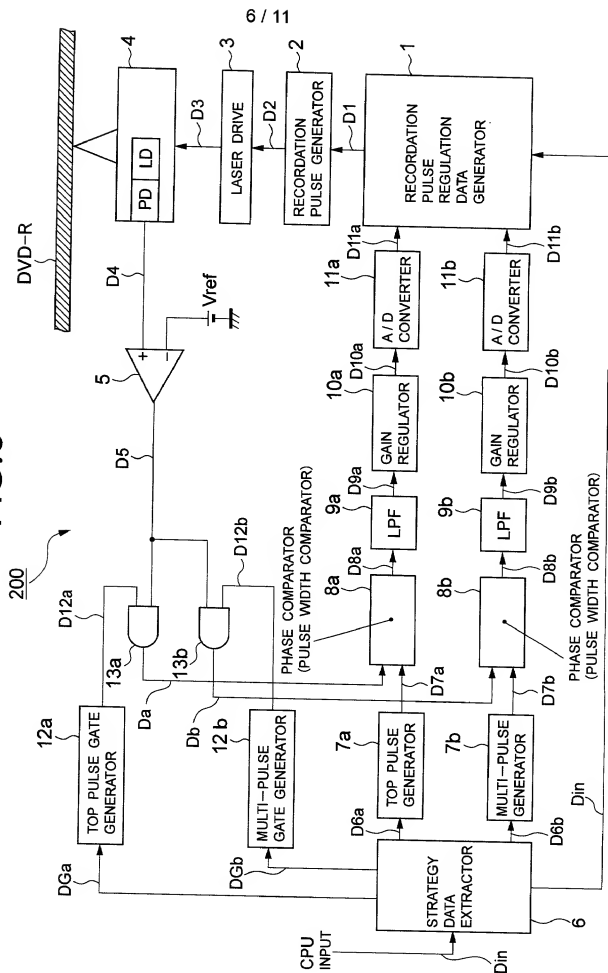


FIG. 8



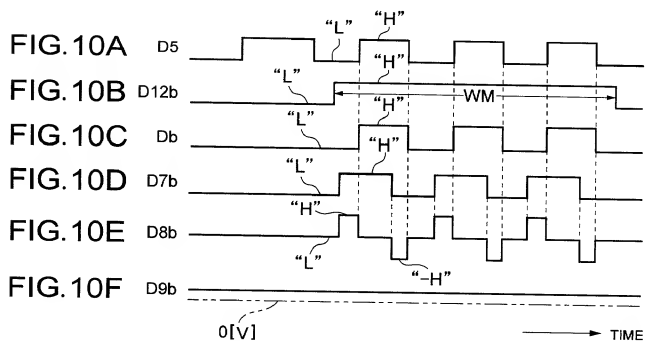
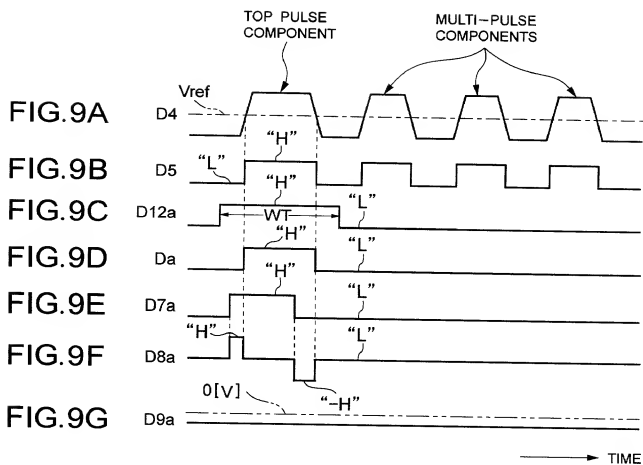
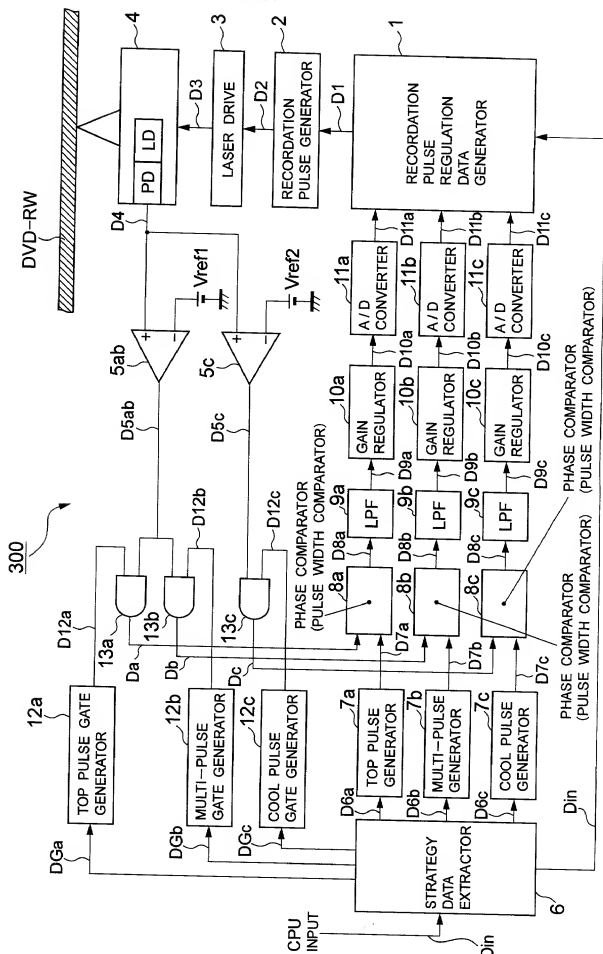
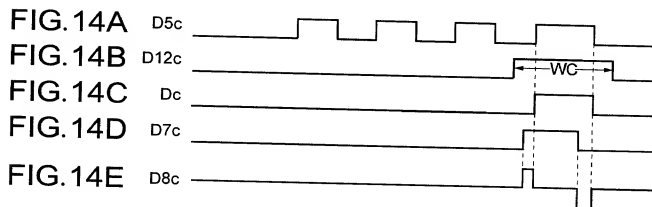
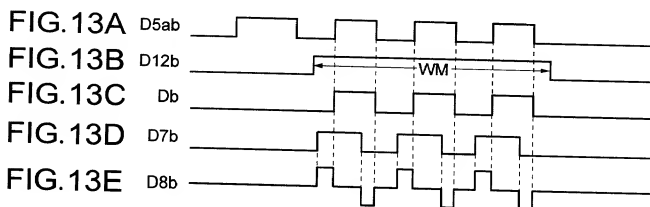
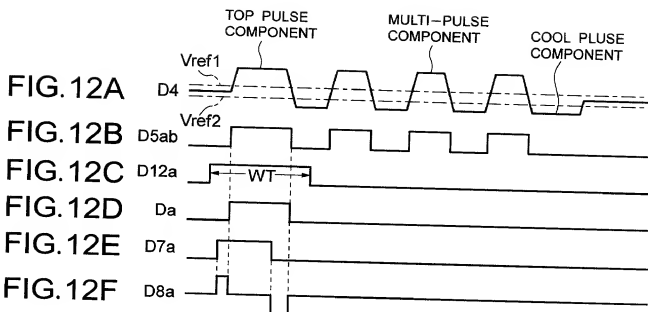


FIG. 11





→ TIME

FIG.15A

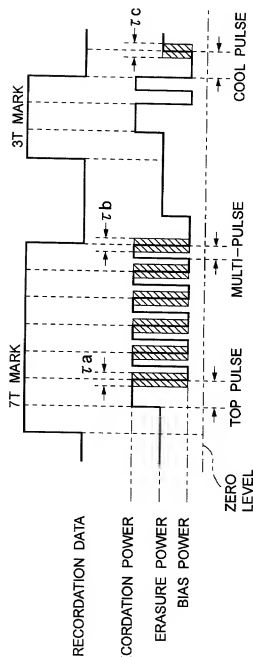


FIG.15B

FIG. 16

